

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				<i>Complete if Known</i>	
				Application Number	10/813,226
				Filing Date	March 30, 2004
				First Named Inventor	Honary, Hooman
				Art Unit	2616
				Examiner Name	Chan, Sai
Sheet	1	of	1	Attorney Docket No: 80107.115US1	

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Filing Date If Appropriate
		US-20050203988A1	09/15/2005	Nollet, V. et al.	11/24/2004
		US-20040187088A1	09/23/2004	Cambonie, J.	02/03/2004
		US-7200837	04/03/2007	Stevens, C.	08/21/2003
		US-6237054	05/22/2001	Freitag, Jr, W. W.	09/14/1998
		US-6266797	07/24/2001	Godfrey, G. M., et al.	11/14/1997
		US-7072382	07/04/2006	Dent, P. R.	04/29/2002
		US-6272188	08/07/2001	Mobin, M. S., et al.	05/08/1998
		US-5987490	11/16/1999	Alidina, M. M., et al.	11/14/1997
		US-20050025222A1	02/03/2005	Underbrink, P. A., et al.	06/16/2004
		US-20050187998A1	08/25/2005	Zheng, L. et al.	02/20/2004
		US-6820102	11/16/2004	Aldrich, B. C., et al.	07/29/2003
		US-6260055	07/10/2001	Sugeno, Y. et al.	10/15/1998
		US-5848096	12/08/1998	Shou, G. et al.	09/06/1996
		US-6397240	05/28/2002	Fernando, J. S., et al.	02/18/1999
		US-7119576	10/10/2006	Langhammer, M. et al.	06/18/2004

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of cited Document	T ²
		JP-2003-244249	08/29/2003	TACHIKAWA, H	
		EP-1311073A1	05/14/2003	Dent, P. R.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
		ALBANESI, M. G., et al., "A VLSI 128-Processor Chip for Multiresolution Image Processing.", <u>Massively Parallel Computing Systems</u> , (1994),296-307			
		ZHANG, H. et al., "Interconnect Architecture Exploration for Low- Energy Reconfigurable Single-chip DSPs", <u>VLSI'99.Proceedings IEEE computer society workshop</u> , (1999),			

EXAMINER

DATE CONSIDERED